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1/16/04

PTO/SB/08A

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
Sheet 2 of 2

Application Number.: 09/838,057
 Filing Date: April 19, 2001
 First Named Inventor: Armstrong et al
 Art Unit: 2182
 Examiner Name: Unassigned
 Attorney Docket Number.: ROC920000252US1

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and or country where published.	T ²
VN	C	Menasce, D. et al. "Capacity Planning and Performance Modeling", ISBN 0-13-035494-5, © 1994.	
VN	D	Leutenegger et al. "A Modeling Study of the TPC-C Benchmark", Proceedings of the 1993 ACM SIGMOD Int'l Conference on Management of Data, 1993, pp. 22-31.	
VN	E	Levine, C. "Order-of-Magnitude Advantage on TPC-C Through Massive Parallelism", Proceedings of the 1995 ACM SIGMOD Int'l Conference on Management of Data, 1995, pp. 464-465.	
VN	F	IBM Corporation, "AS/400 Logical Partitions Hardware Planning Guide", © 1999	
VN	G	Schimunek, G. et al. "Slicing the AS/400 With Logical Partitioning: A How to Guide", August 1999.	
VN	H	IBM Corporation, "LPAR Configuration and Management", First Edition, © April 2002.	
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		JAN 21 2004	
		Technology Center 2100	

Examiner Signature	VAN H. NGUYEN	Date Considered	7/20/04
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional).

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Sheet 1 of 2

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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number Number - Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns or Lines Where Relevant Passages or Figs. Appear
VN	A	US - 5535321A	07/09/1996	Timothy J. Massaro et al	
VN	B	US - 6625638B1	09/23/2003	Jeffrey Paul Kubala et al	

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FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	Foreign Patent Document Country ³ - Number ⁴ - Kind Code ⁵ Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns or Lines Where Relevant Passages or Figs. Appear	T ⁶

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⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet 1 of 2

OCT 20 2003

Application Number.: 09/838,057
 Filing Date: April 19, 2001
 First Named Inventor: Armstrong et al.
 Art Unit: 2823
 Examiner Name: Unassigned
 Attorney Docket Number.: ROC920000252US1

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number Number - Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns or Lines Where Relevant Passages or Figs. Appear
VN		US - 5,692,193	11-25-1997	Jagannathan et al.	
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DISCLOSURE
APPLICANT
OCT 20 2003
PATENT OFFICE
PRIOR ART

Application Number.: 09/838,057
Filing Date: April 19, 2001
First Named Inventor: Armstrong et al.
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Attorney Docket Number.: ROC920000252US1

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4/19/01

FORM PTO-1449 (Modified)

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S
INFORMATION
DISCLOSURE STATEMENTATTY. DOCKET NO.
ROC9-2000-0252-US1

SERIAL NO. (Unassigned)

APPLICANT: William J. Armstrong, et al.

(Use several sheets if necessary)

Page 1 of 1

FILING DATE: (Herewith)

GROUP: (Unassigned)

H1002 U.S. PTO
09/838057
04/19/01

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		DOCUMENT NUMBER							DATE	NAME	CLASS	SUB- CLASS	FILING DATE (IF APPRO.)
VN	AA	5	6	5	9	7	8	6	8/19/1997	George, et al.	395	653	
VN	AB	4	8	4	3	5	4	1	6/27/1989	Bean, et al.	364	200	
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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION	
													YES	NO
	AL													
	AM													
	AN													
	AO													
	AP													

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

VN	AR	U.S. Patent Application Serial No. 09/672,043, entitled "Technique for Configuring Processors in System With Logical Partitions", filed September 29, 2000.
VN	AS	IBM Corporation, S/390 Processor Resource/Systems Manager Planning Guide (IBM Pub. No. GA22-7236-04, 5 th Edition, March, 1999)
	AT	

EXAMINER

VAN H. NGUYEN

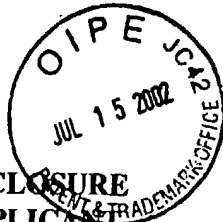
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Sheet 1 of 1Application Number.: 09/838,057Filing Date: April 19, 2001First Named Inventor: Armstrong et al.Art Unit: 2182Examiner Name: UnassignedAttorney Docket Number.: ROC920000252US1COPY OF PAPERS
ORIGINALLY FILED09/838,057April 19, 2001Armstrong et al.2182UnassignedROC920000252US1

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number Number - Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns or Lines Where Relevant Passages or Figs. Appear
VN		US - 5,325,525	06-28-1994	Shan et al.	
VN		US - 5,504,670	04-02-1996	Barth et al.	
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8/18/03

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VN		US - 5,222,215	06-22-1993	Chou et al.	
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